(4)

| REV | REVISION DESCRIPTION | DATE | APPROVED |
|---|---|---|---|
| 1 | PRELIMINARY RELEASE | | |
| 2 | REVISED | | |
| 3 | REVISED | | |
| 4 | CHANGES -27 & -28 | | |

2. RESISTORS ARE MEASURED IN OHMS, 5%, 1/4 WATT.
1. CAPACITORS ARE MEASURED IN MICROFARADS.
NOTES-UNLESS OTHERWISE SPECIFIED:

| | | | |
|---|---|---|---|
| USED ON: SH 205 | DRAWN BY: S. Yamada 3-5-87 | ATARI (JAPAN) CORPORATION | |
| NEXT ASSY: CA200029-XXX | CHECKED | Taranomon Kiyoshi Building 4-3-10, Taranomon Minato-ku, Tokyo 105 | |
| MATERIAL | ENGINEER | TITLE: SCHEMATIC DIAGRAM (SH205) | |
| FINISH | APPROVED | SIZE: D | DRAWING NO: C100171 |
| | APPROVED | SCALE: NONE | SHEET 1 OF 2 |